

REMARKS

Applicants appreciate the allowance of Claims 3, 6, 9, 14, 17, 20, 23 and 27. Applicants have the following response to the remaining objections and rejections in the Office Action.

Cited References

Initially, it is noted that during the Office Action, the Examiner included a PTO-892 form citing "US-4,206,450 Hardin". Such a reference was not included with the PTO-892 or Office Action. Instead, the Examiner included US-4,206,460 Yasuda et al. After a telephone conference with the Examiner, it was learned that Yasuda et al. should have been listed on the PTO-892 and not Hardin. Accordingly, it is requested that a corrected PTO-892 form be issued.

Claim Objections

In the Office Action, the Examiner objects to the claims as including reference characters which are not enclosed within parentheses. Applicants have amended the claims and submit that they are no longer objectionable. Accordingly, it is requested that this objection be withdrawn.

Claim Rejections – 35 USC §112

The Examiner also rejects Claims 1, 2, 4, 5, 7, 5(?), 12, 13, 15, 16, 18, 19, 21, 22, 25 and 26 under 35 USC §112, second paragraph as being indefinite.

In order to advance the prosecution of this application, Applicants have amended, for example, Claim 1 to delete the limitations of

"wherein the address (writing) period overlaps with the sustain (lights-on) period in at least one sub-frame period of the n sub-frame periods, and wherein in the case

where an address (writing) period T_{a_m} ($1 \leq m \leq n$) of a sub-frame period SF_m overlaps with an address (writing) period $T_{a_{m+1}}$ of a sub-frame period SF_{m+1} , a clear period T_{c_m} is provided which starts upon completion of a sustain (lights-on) period T_{s_m} of the sub-frame period SF_m and ends upon start of the address (writing) period $T_{a_{m+1}}$ ”.

Applicants are amending Claim 1 to include the recital

“inputting a first signal to a pixel comprising a light emitting element from a source signal line during each address period, wherein a capacitor storage line is maintained at a first potential; turning on the light emitting element during each sustain period, wherein the capacitor storage line is maintained at the first potential; providing a clear period T_{c_m} during a period from an end of the sustain period T_{s_m} ($1 \leq m \leq n$) of a sub-frame period SF_m through until a start of the address period $T_{a_{m+1}}$ of a sub-frame period SF_{m+1} , wherein the capacitor storage line is maintained at a second potential”.

Such an amendment is supported, for example, by Embodiment 1 and Figs. 2 and 20 of the present application.

Claims 2, 12 and 13 have been amended in a similar fashion.

Accordingly, it is respectfully submitted that these amendment overcome the §112 rejection, and it is requested that it be withdrawn.

Claim Rejections - 35 USC §103

The Examiner also rejects Claims 10, 11 and 24 under 35 USC §103(a) as being unpatentable over Kondo. This rejection is respectfully traversed.

Kondo discloses that capacitor storage line 14 is always maintained at "0" voltage as a reference voltage, see e.g. [0004] and [0044-0046] on pages 12 and 31-32 of Kondo, and does not disclose capacitor storage line driver circuit, as required in amended claim 10 which recites "a potential of the capacitor storage line changes in accordance with a signal inputted from the capacitor storage line driver circuit".

Accordingly, Applicants respectfully submit that the claimed invention is not disclosed or suggested by the cited reference and is patentable thereover. Therefore, it is requested that this rejection be withdrawn.

Conclusion


Accordingly, the present application is in a condition for allowance and should be allowed.

If any fee is due for this amendment, please charge our Deposit Account 50/1039.

Favorable reconsideration is earnestly solicited.

Respectfully submitted,

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